

M0 declaration for RF

Product: Obsidian

Part project leader: Lind Moeller Mortensen

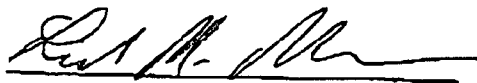
Risks:

The B6PLA platform solution has spurious emissions in TX causing marginal modulation spectrum performance. Changes are currently being implemented in the B6PLA platform to improve the performance. It is expected that the platform will reach a solution with sufficient margin to meet the GSM specification. This is however seen as a risk until it has been verified that the implemented changes are sufficient to secure the performance.

Appendices: M0 checklist for RF

M0 is hereby declared for RF

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M0 Declaration RF
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Obsidian M0 Statement for RF

References

Enclosures

Revision Information

Created:	Revision	Name	Comments
28.11.05	0.1	BMLLLLM	Initial revision
Modified			

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1 Introduction

This document contains the M0 statement from the RF department.

2 Results and open issues M0

1. Project leader HW (PL HW)/ part project leader (PPL RF) has been designated by the department head	Lind Møller Mortensen
2. Detailed Primavera project plan from M0 to M1 showing dedicated resources by name is available	Done
3. Rough Primavera project plan from M1 to M3/S4 showing dedicated resources on department level is available	Done
4. Assembly concept for mechanical design dependent components or modules coordinated and agreed with MD, dHW and EA (e.g. display, camera, acoustics, vibra, battery, card readers, IrDa, LED flash, connectors, etc.)	Done. Mechanical design is however not 100% fixed.
5. Product requirements as specified in the M0 document achievable with available technologies / know-how by the specified S3 HW deadline	OK
6. Product relevant pre-/component developments defined. Time schedules and deliveries aligned to product development schedule	OK
7. Second solutions and requirements to multiple sources agreed with Strategic Procurement	No second solution for RF.
8. Necessary test tools and testing concept agreed with SW; availability of tools committed by SW	OK
9. Platform released for start of product integration available i.e. platform master schematics, porting guide etc	Partly OK. Master schematics available. Porting guide partly available. Changes are currently being implemented in the B6PLA platform to improve performance.
10. First circuit schematics extracted from master schematics	Done
11. Platform features/configurations supported by product agreed with PL SW	Done
12. Product related deviations from the platform defined and agreed with involved parties i.e. SW, MD etc	No deviations from platform.

3 Conclusion

The RF department declares the M0 milestone.