

Schedule/PV/Milestones:

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| ▪ M0: 01.04.05 | ▪ S2: 26.09.05 | ▪ DS: 14.02.06 |
| ▪ S0: 26.04.05 | ▪ S25: 24.11.05 | ▪ S4: 28.02.06 |
| ▪ M1: 03.06.05 | ▪ S3: 10.01.06 | ▪ M3: 28.03.06 |

Major Issues

- Discussion about switching to SG2 solution still ongoing due to the fact that a lot of one-time L1 effort would be necessary but from today's point of view SG2 Rel. 2 in 02/06 is highly endangered (see Polaris Status Report)
- Current design is based on a rotated display which is not possible without either changing the display modules or use a different speaker – investigations ongoing

HW

- Updated AAC-Chip (SACC-X) required with included SD-Interface and 26MHz SPI. Schedule risky but feasible.

SW

- High one-time effort for support of Nano-I/O, AAC chip with SD interface, display interface not via video chip.
- Only support of 130x130 display resolution possible as no SGL-based MMI available for 128x128 resolution

