SGL Products Phoenix/Sirius

M0

Status 5. March 2005 Platform & Products
75 SGL

Phoenix/ Sirius



PV-Milestones Phoenix/Sirius:

10.06.04 / 10.06.04 \

| ■ S0 | 10.09.04 / 03.09.04 🗸 |
|-------------|-----------------------|
| ■M1 | 25.10.04 / 05.11.04 🗸 |
| ■S15 | 18.11.04 / 12.11.04 🗸 |
| ■ S2 | 26.01.05 / 08.02.05 🗸 |
| ■S25 | 06.04.05 / 14.04.05 |

- Pilot Start 09.05.05 / 23.05.05
- ■DS SW Freeze 13.05.05

| ■ S3 | 20.05.05 / 25.05.05 |
|-------------|---------------------|
| ■PS | 30.05.05 / 06.06.05 |
| ■DS | 10.06.05 / 28.06.05 |

Highlights

- DS still feasible.
- HW and SW well prepared for CeBIT.
- SW stability one week after S25 SW Freeze already remarkable good.
- AAC+ working well (HW and SW).

<u>Info:</u>

• Increase of Sirius Development Cost in clarification.

Major threats:

- Temp.-Control-Bug in Intel Memory (Flash/SRAM) may lead to decreased standby time.
 - => Countermeasures under evaluation.
- Some Key Functions of the Project Team staffed with less Experienced Colleagues.
- SW-Maturity at Operator Approval / DS.
 - => Help from experienced Experts & Commitment form Mario Topel to reach DS.
- Belated arrival of B1+ prototypes from Germany: schedule of Phoenix-MD jeopardized, could result in delay of DS => counter measures under evaluation.
- Misalignment of Phoenix-Joystick => Countermeasures under Evaluation.
- Lead Free Soldering Process seems not to be stable yet (Mozart and I/O-Connector).
- B1 done, B1+ ongoing, no change form B1+ to B2 Layout planned.

