

Schedule/PV/Milestones:

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|-----------------------------|-----------------|----------------|
| ▪ M0: 22.10.04 (✓ 25.10.05) | ▪ S2: 29.03.05 | ▪ DS: 11.07.05 |
| ▪ S0: 29.10.04 (✓ 01.11.05) | ▪ S25: 27.04.05 | ▪ S4: 11.07.05 |
| ▪ M1: 10.12.04 (✓ 23.12.05) | ▪ S3: 30.05.05 | ▪ M3: 08.08.05 |

- DS deviation of current PV plan is 3 days

Major threats

- Further delay of B1+ tape out => potential delay of DS

HW

- B1+ tape out delayed by 2 days from 09.03.05 to 11.03.05 because root cause for high power consumption in talk and idle and countermeasures for self-interference and for low frequency emissions via headset have not found been found.

SW

- Due to special CeBit SW preparation all bug fixes have to be copied from the stabilization branch to the normal CDB – additional unplanned effort but no effect on timeline

Mechanics

- Hard tool parts planned to be used in B1+ were already used for B1.2 – first tests indicated good quality

Prototyping:

- See HW/Major threats

