

# EGL products predevelopment

## Platform & Products

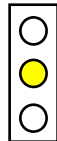
Status  
15. Mar. 2005 75 & 85 EGL

EGOLDlite is an cost optimized baseband processor and based on a shrunk EGOLD V3 and SGOLDlite DSP targeting low tier products.

The development of the EGOLDlite is carried out in a pre-development project which covers EGL HW, SW and FW. The purpose of the predevelopment is to verify and test EGL for the secure application in product products.

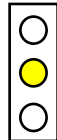
Lead product projects are LIBRA/DORADO with M3 05/05.

### Timeline



I0 09.04.2004 ✓ I3 18.03.2005  
I1 08.06.2004 ✓ I4 29.04.2005  
I2 13.08.2004 ✓  
- I3 declaration is delayed due to several SW bugs found in Libra B6E B1+ devices  
- SW debugging with Libra B6E B1+ is ongoing.

### Software platform



- All features of Libra were implemented except for AMR link adaptation.  
- AMR link adaptation implementation is ongoing on Comneon side.  
- S25 for Libra B6E int'l variant will be declared in cw11, 2005.  
- For Libra, bugfixing is ongoing.

### Hardware platform



→ X75 HW platform does not include HW which is explicitly used in EGOLDlite products.  
Such components (e.g. B/W display → DORADO, slim 130 x 130 TFT display → FLAMINGO, ...) are tracked in the related EGL product projects.

### EGOLDlite



EGOLDlite baseband chipset qualification is almost done, including:  
- The TEAKlite DSP qualification is done and debugging is almost done.  
- GPRS qualification is ongoing and will be completed in cw14.  
- RRs of EGOLDlite in Libra and Dorado was signed

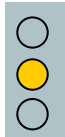
# EGL products

## Libra (A75) – lead project of the EGL platform

### Platform & Products

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Libra



#### Schedule/PV/Milestones:

M1	28.10.04 done	S3	22.04.05	RU	09.05.05
S2	25.02.05 done	Pilot	18.04.05	DS	20.05.05
M2	18.03.05 %	PS	09.05.05	S4	03.06.05
S25	18.03.05			M3	30.06.05

#### Status next milestone:

S25 (18.03.): blocked by big power consumption and current existing SW bugs (ST & SW).

#### Major threats

- big power consumption unclear if SW or HW, bugs such as 474E (S25 relevant for ST), etc.. SW and COMNEON are investigate in parallel.

#### HW

- Device selection will started on March 15. TA planned for March 28.
- The qualification of some DTC components (Wintek LCD 1USD, PCM lest battery (0.18 EUR), ...) is ongoing.

#### SW

- Last weeks blocker: AMR link adaptation will be implemented by COMNEON & SIEMENS!  
Ongoing,

#### Mechanics

- Keypad (gap between function keys and housing bridge) need to be further improved in 3<sup>rd</sup> OT.  
Ongoing.

#### Prototyping:

- B1+ run is done, B2 population and test will start on Mar. 22, 2005

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# EGL products Dorado

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### Schedule/PV/Milestones:

▪ M0 07/14/04 ✓	S15 11/24/04 ✓ 11/25/04	S3 04/14/05 ->05/19/05
▪ S0 07/30/04 ✓	S2 01/19/05 ->03/18/05	DS 05/19/05 ->06/16/05
▪ M1 09/28/04 ✓ 10/25/04	S25 02/25/05 ->04/14/05	S4 05/26/05 ->06/30/05

### Status next milestone:

S2 (03.18): blocked by power consumption (same as Libra)

### Major threats

- Big power consumption, critical bugs etc. (same as Libra)
- SW booting problem must be solved before Approval Starts (Apr. 12)

### HW

- Base band, RF, LPD, EA: S2 status reached.
- EMC problem (Self interference) found. Solution in Libra available. Has to be confirmed in Libra B2.

### SW

- Last weeks blocker: AMR link adaptation will be implemented by COMNEON & SIEMENS!

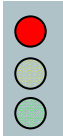
### Mechanics

- S2 status reached

### Prototyping:

- B1+ 1<sup>st</sup> run started on Mar. 10.

Dorado



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# EGL products

## Pegasus

### Platform & Products

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#### Schedule/PV/Milestones:

▪ M0	23.07.04 ✓	S15	15.11.04 ✓	S3	20.05.05
▪ S0	17.08.04 ✓	S2	18.03.05	DS	17.06.05
▪ M1	28.09.04 ✓	S25	31.03.05	S4	01.07.05
				M3	22.07.05

#### Status next milestone:

S2 (18.03.): Blocked due to high power consumption (ST, SW, CCQ).

#### Major threats

- big power consumption unclear if SW or HW. Some bugs such as 474E (S25 relevant for ST), etc.. SW and COMNEON are investigate in parallel.
- Reference sensitivity endangers device selection and TA. HW & SW investigating. Most probably related to the SALZBURG ASIC.

#### EMC/Antenna

- B1+ shows good improvement regarding spurious emission but still optimization necessary. New self interference found.

#### SW

- Last weeks blocker: AMR link adaptation will be implemented by COMNEON & SIEMENS!

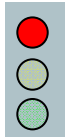
#### ST

- High Power consumption ST S2 blocking point.
- Ref. Sensitivity critical regarding start of TA.

#### Prototype run:

- B1+ ongoing, IRDA testing is fully implemented in prototype run.

Pegasus



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## Flamingo

### Platform & Products

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#### Schedule/PV/Milestones:

▪ M0 10.12.04 done	S2	26.05.05	S4	24.10.05
▪ S0 23.12.04 done	S25	21.07.05	DS	24.10.05
▪ M1 28.01.05 done	S3	30.08.05	M3	25.11.05

#### Major threats

- New components (display, Speaker box, ...) critical regarding aggressive time line.
- There is a problem with color separation on the slider part in BALDA, which is being under investigation and the solution needs to be found out by BALDA, MD, PM and the designer together ASAP.

#### Status next milestone:

S15 will be declared on March 17, no big issue at this moment.

#### HW

- A1 prototype is under evaluation. No big issues have been found so far.

#### SW

- B1 software development is under development and on the right track.

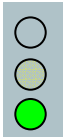
#### Mechanics

- 100% of MD data has been sent to the supplier – Balda on 28/02/05 for soft tool and hard tool making. There is a problem with color separation on the slider part in Balda, which is being under investigation and the solution needs to be found out.

#### Prototyping:

- A1 prototype shop is completed. B1 TO is 14/03/05.

Flamingo



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## Flamingo Milestones Update

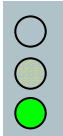
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M1 milestones:

	PD Team schedule	Official PSR
M0	12/10/04	
S0	12/23/04	
M1	01/21/05	
S15	03/17/05	
S2	05/26/05	
S25	07/21/05	
S3	08/30/05	
PR	08/31/05	
PS	09/21/05	
DS	10/05/05	10/24/05
S4	10/19/05	11/07/05
M3	11/08/05	11/25/05

Flamingo



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#### Schedule/PV/Milestones:

- M0 28.02.05 declaration blocked due to new antenna concept (→ March 18 expected)
- S0 21.04.05 S3 19.12.05
- M1 19.05.05 DS 24.01.06
- S25 14.11.05 S4 24.01.06

According PV planning (M0-M1 detailed and M1 – M3 rough).

#### Major threats

- New antenna concept (slim line, antenna in south) under development. Feasibility not finally proven in Quail. Actually looks quite promising. (→ March 18).

#### Status next milestone:

M0 (18.03.): EA declaration blocked as antenna antenna concept is still under investigation. (→ March 18)

#### HW

- 3 layers FPC will be executed and 2 layers FPC stand by

#### SW

- Requested feature set exceeds available memory (RAM)->estimation show RAM will be enough after optimization, and other optimization will be performed to improve performance since long time for S0.

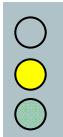
#### Mechanics

- Mockups/SLA parts for antenna development available in EA.->2<sup>nd</sup> Mockup is available in EA

#### Prototyping:

- A1 TO scheduled 06.05.05-> depending on EA input

Quail



# Backup



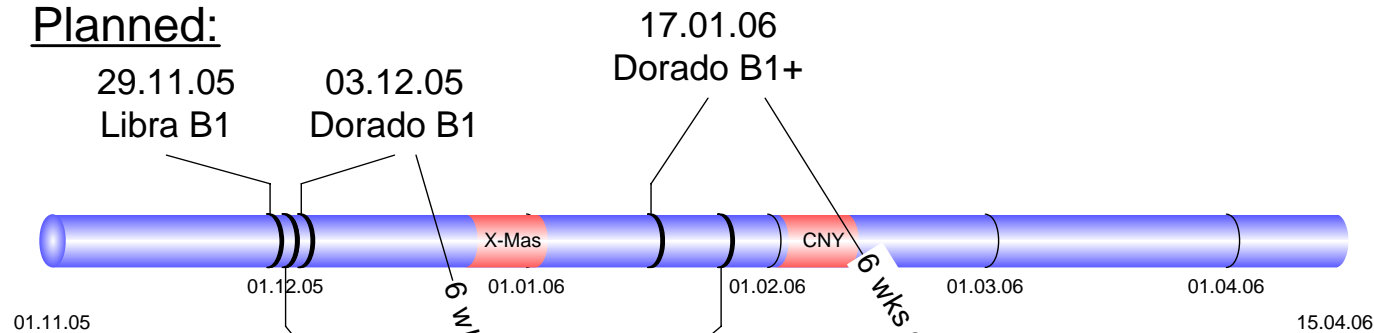
# EGL products

## Pegasus & Dorado Sample Shop Schedule

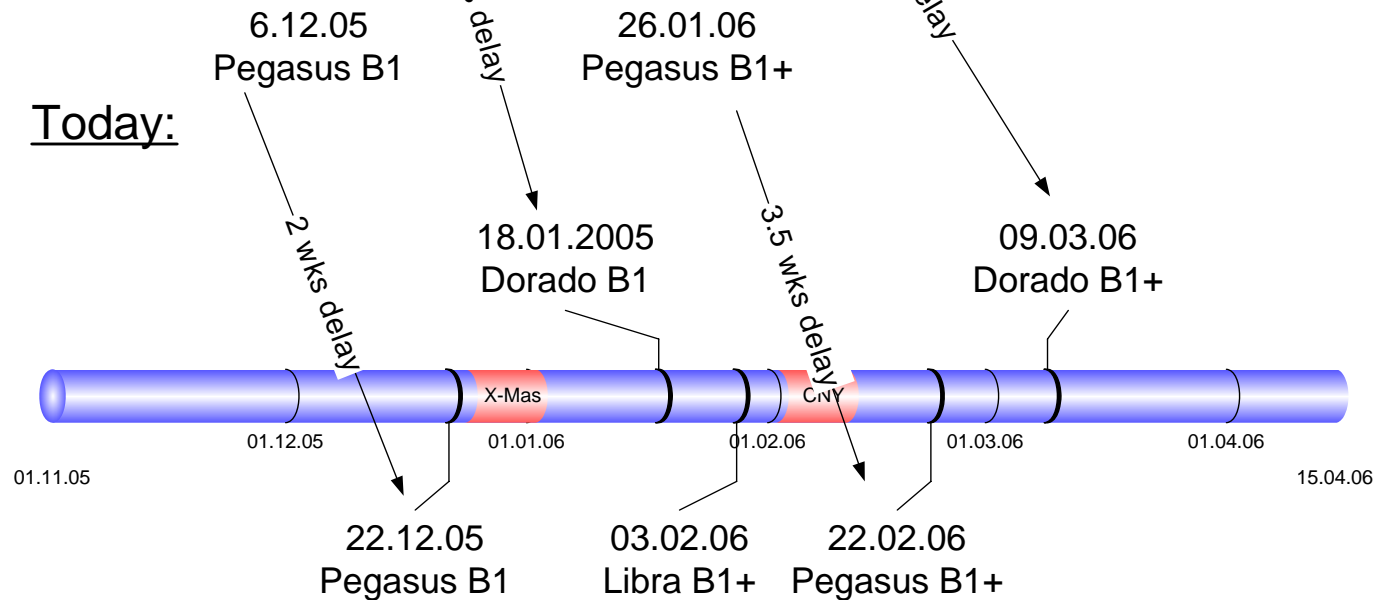
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### Planned:



### Today:



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